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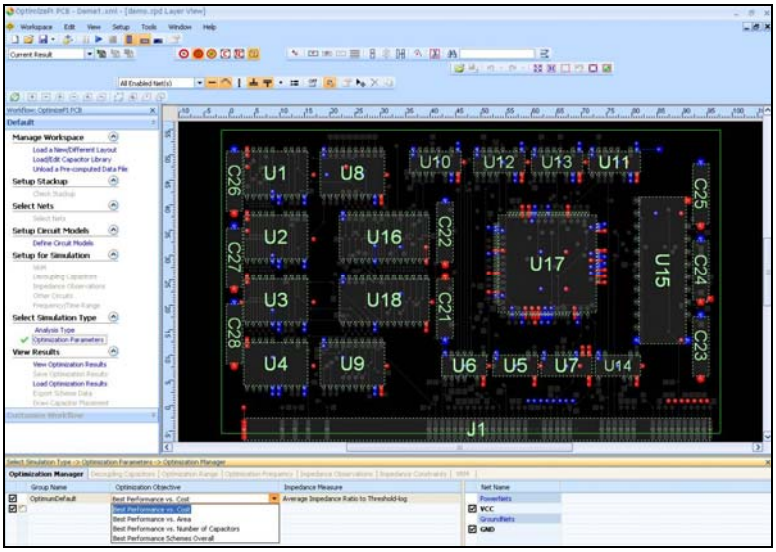
Sigrity Introduces OptimizePI™ Version 1.2 Beta

SANTA CLARA, Calif. – Sept. 19, 2008 – Sigrity has added significant new productivity features to its award-winning OptimizePI product to enable designers to meet PCB and IC package power delivery system impedance targets in record time. Pre-layout assessments are simplified and a single-click option has been added to guide optimization targeting for performance, decap count, area or cost.



With OptimizePI 1.2 component tolerances are taken into account for refined accuracy and loop inductance can be viewed at every component location. When there is a specific targeted frequency, such as 800 mhz based on memory interface requirements, OptimizePI now supports a specific user defined impedance level.

OptimizePI helps board design teams address changes in the approach semiconductor companies are using with respect to design guidelines. Up to now, guidelines for board designers were very specific with respect to the number, value and exact location of decoupling capacitors. Now, there is a trend away from this specification style to providing an impedance target to hit for operations between specified frequency ranges. This approach gives more flexibility to design teams. At the same time, it increases the need for capabilities to analytically assure the design measures up. OptimizePI uniquely offers automation that supports this approach.

OptimizePI was introduced in 2007. It was the first - and remains the only - power integrity tool to explicitly consider design cost and include automated optimization. OptimizePI relieves designers of the burden of best-guessing decap placement in their designs or resorting to unnecessarily robust designs. Decap cost savings of 50 percent have been observed.

For more on OptimizePI: http://www.sigrity.com/products/optimizepi/optimizepi.htm

About Sigrity

Sigrity, Inc., a privately held U.S. company incorporated in 1998, delivers advanced software solutions for package physical design and for analyzing power and signal integrity in chips, packages and printed circuit boards. Sigrity's patented electrical analysis methodologies run orders of magnitude faster than general-purpose electromagnetic tools, helping leading companies in the semiconductor, computer, graphics, communications and networking industries ensure high performance and reduce time to market. The company is headquartered in Santa Clara, Calif., with direct sales and global distribution through worldwide representatives. For more information about how to ensure operational designs by using Sigrity's package physical design and power and signal integrity analysis solutions, please visit: http://www.sigrity.com.