
Sigrity Adds Enhancements to Product Lineup

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Sigrity has added new functionality to its signal and power integrity analysis tools. Users will see enhancements to SPEED2000, PowerSI, PowerDC, Broadband SPICE, OptimizePI, XtractIM and Unified Package Designer (UPD).

Sigrity introduced user-customizable workflows with 8.0 beta to enhance productivity for both new and experienced users. Sigrity is providing seven default workflows for use with PowerSI and SPEED2000 covering topics such as a model extraction workflow, power ground noise simulation workflow and EMC/EMI simulation workflow. Sigrity workflows can be tuned to adapt to individual / team preferences and users can also easily create their own workflows.

The links included below go to the Sigrity Customer Sign-In area (SPDnet). Primary user contacts have account user name and password information. Password retrieval is available at the site.

SpeedXP (8.0 Beta)

<http://www.sigrity.com/spdnet/softwareupdate/speedxp>

Overall SpeedXP capability:

- Customizable workflows ... for user defined and Sigrity provided out-of-the box workflows to streamline user activity
- Model Connection Protocol (MCP) ... Sigrity-developed open format that is useful to easily connect chip, package and board models
- Material library for metals and dielectrics ... for efficient handling of temperature dependent material effects
- Unified circuit manager ... for dockable viewing panes with typical functions and views, consistent across products
- Usability options ... Tip of the Day, layout editing, via shape handling, zoom control, image export

SPEED2000 users will see:

- SPEED2000 provided workflows ... for SSO simulation, power/ground noise simulation, EMC/EMI simulation, device model simulation
- Waveform analysis functions ... eye diagram extensions, waveform linking and measurement enhancements
- Trace voltage visualization ... signal waveform propagation through a net with 3D animation

PowerSI users will see:

- PowerSI provided workflows ... for model extraction, hot spot (resonance detection), EMC/EMI simulation
- Broadband Network Parameter (BNP) viewer ... for visualization and post processing of extracted data
- Compressed S-parameter output ... produces file sizes 100x smaller than zipped TouchStone files

PowerDC users will see:

- Multi-board analysis ... streamlines broader system IR drop assessment and DC sign-off
- SPICE model support ... to import and generate SPICE models with a wizard that includes MCP header information
- Usability options ... plane current density vector display, constraint management simplification

BroadbandSPICE users will see:

- Enhanced accuracy ... improved impedance fitting for DC at open ports
- Flow support ... automatic launch from within PowerSI

XtractIM / SpeedPKG Suite (1.3 Production)

<http://www.sigrity.com/spdnet/softwareupdate/speedpkg>

Improvements to XtractIM in the newest production release:

- Die-to-die wirebond extraction ... supports extraction for additional SiP implementations
- Leadframe package extraction ... incorporates leadframe metal and wirebonds, DXF import and set-up

OptimizePI / SpeedPCB Suite (1.1 Production)

<http://www.sigrity.com/spdnet/softwareupdate/speedpcb>

Improvements to OptimizePI in the newest production release:

- Time domain verification ... confirmation of impedance based assessment results
- Enhanced decap library options ... library wizard, search functions, external and project specific decap library support
- More decap selection flexibility ... name-based and mouse controlled group editing
- Peak resonance handling ... new linear-based approach in addition to logarithm based method for reducing peaks
- Usability options ... what-if assessment improvements for visualizing multiple elements and selective incremental reset

Unified Package Designer (8.0 Production)

<http://www.sigrity.com/spdnet/softwareupdate/upd>

Improvements to UPD in the newest production release:

- High speed design ... skew monitoring
- Enhanced package extraction ... automated XtractIM launch from inside UPD
- Free UPD design viewer UI ... simplified navigation for casual users

About Sigrity

Sigrity, Inc., a privately held U.S. company incorporated in 1998, delivers advanced software solutions for package physical design and for analyzing power and signal integrity in chips, packages and printed circuit boards. Sigrity's patented electrical analysis methodologies run orders of magnitude faster than general-purpose electromagnetic tools, helping leading companies in the semiconductor, computer, graphics, communications and networking industries ensure high performance and reduce time to market. The company is headquartered in Santa Clara, Calif., with a direct sales force and representatives worldwide. For more information about how to ensure operational designs by using Sigrity's package physical design, and power and signal integrity analysis solutions, please visit <http://www.sigrity.com/>.